

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Masanori TERAHARA, et al.

Serial Number: Not Yet Assigned

Filed: October 29, 2003

Customer No.: 38834

For: SEMICONDUCTOR DEVICE FABRICATION METHOD

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

October 29, 2003

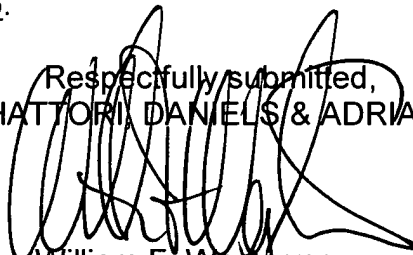
Sir:

In compliance with 37 CFR 1.56, Applicant(s) call(s) to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

Copies of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,
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Enclosures: PTO-1449; References(3)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Attv. Docket No. 032067	Serial No. New Appln.
	Applicant(s): Masanori TERAHARA, et al.	
	Filing Date: October 29, 2003	Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
	AA					
	AB					
	AC					
	AD					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)	
AE	2000-269192	09/29/2000	Japan	Abstract, Discussed in the Spec.
AF	2001-44274	02/16/2001	Japan	Abstract, Discussed in the Spec.
AG	2001-176841	6/29/2001	Japan	Abstract, Discussed in the Spec.
AH				
AI				

OTHER DOCUMENTS

	AJ	
	AK	
Examiner	Date Considered	